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(71) Applicant (for all designated States except US): **KONINKLIJKE PHILIPS ELECTRONICS N.V.** [NL/NL];
Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **SVECHTAROV, Jordan, K.** [BG/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). **VAN DER ZANDEN, Josephus, H., B.** [NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

(74) Agent: **DUIJVESTIJN, Adrianus, J.**; Philips Intellectual Property & Standards, Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

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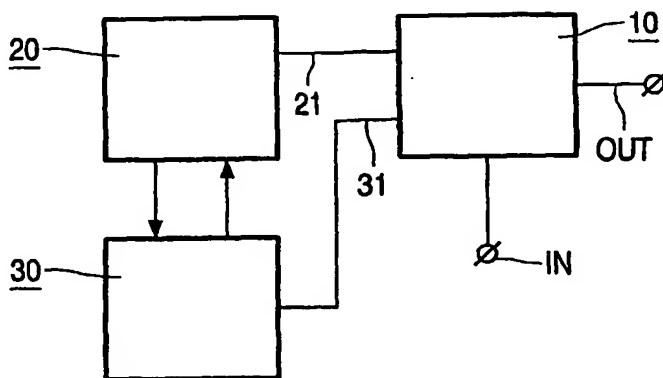
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(54) Title: **AMPLIFIER BIAS CIRCUIT METHOD FOR BIASING AN AMPLIFIER AND INTEGRATED CIRCUIT COMPRISING AN AMPLIFIER BIAS CIRCUIT**



(57) Abstract: An amplifier bias circuit connectable to an amplifier device, comprising a first sensor device for sensing a first amplifier characteristic and for providing at a first sensor output a bias signal related to the first amplifier characteristic. The circuit further comprises a second sensor device for sensing a second amplifier characteristic and for providing at a second sensor output a bias signal related to the second amplifier characteristic. The first sensor output and second sensor output are each connected to an amplifier connectable to a bias input of said amplifier device.

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